

Via Arrondello 16

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Giacomo Mabritto

Personal Information

- ◆ Nationality: Italian
- ◆ Date of Birth: 14th October 1979
- ◆ Place of birth: Ivrea
- ◆ Address: Via Arrondello 16, Pecco (TO)

Education

- ◆ *September 2004* Graduate with grade 97/110
- ◆ *2003-2004* Thesis: “*Architetture per codificatori video a bassa complessità*”
- ◆ *1998-2004* Polytechnic University of Turin: ***Master of science in electronic engineering***
- ◆ *1993-1998* High school: Liceo Scientifico “A.Gramsci”, Ivrea (TO)

Training

- ◆ *September 2008* Course of “Linux Embedded”
- ◆ *June 2008* Course of “Programming in ASP.NET”
- ◆ *Novembre 2007* Red Hat Certified Technician
- ◆ *Novembre 2007* Course of Red Hat Enterprise Linux System Administration (RH133).
- ◆ *Novembre 2006* Course of Red Hat Essentials (RH033)
- ◆ *November 2005* Course of Skill.

Professional Skills

- ◆ Hardware description language: VHDL, Verilog, SystemC
- ◆ Use of field programmable Gate Arrays (Xilinx, Lattice, Altera, Actel).
- ◆ Microelectronics: Integrated Systems Architecture.
- ◆ Firmware development for micro controller and embedded system (Zynq, Microblaze, Atmel, ST).
- ◆ Analysis and testing of digital system.
- ◆ Software: Assembler, C, C++, Visual Basic.Net, some experience with PHP and SQL.
- ◆ Operation system: Windows, Linux (RH133), UNIX.

Work Experience

- ◆ Since December 1st, 2013

Thallosjet srl

R&D senior HW designer Manager

Main tasks:

- Responsible for the development of industrial digital printing systems
 - Development of non conventional Ink-Jet technology applications
 - Coordination of the R&D group (4 people)
 - Customer Relationship Management
- ◆ Since November 5th, 2012 to November 31th, 2013

Thales Alenia Space

AIT (Assembly Integration and Test) engineer in satellite platform team of BepiColombo

Main tasks:

- Generation of integration procedures, test procedures, and of associated reports
 - Generation of checkout software (test sequences, synoptics)
 - Support to failure investigations
- ◆ Since December 1st, 2004 to November 31th, 2012

Olivetti – Jet

R&D – Electronic Design

Main tasks:

- Design of biomedical systems with the development of electronic equipment for the deposition of biological substances, their management and analysis by software algorithms for data processing.
- Electronic design, firmware development and software for in field testing systems.
- Software development for production processes automation.
- Design and drafting of photo lithographic masks for silicon foundry, using Cadence-Virtuoso software, focused on inkjet technology systems

Lingue straniere Inglese (PET)

Personal Sport, sci, viaggi, moto.

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selezione e comunicazione ai sensi del D.Lgs. 196 del 30/06/2003